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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/653,701	09/01/2000	Lorne Trotter	10442-10"US" JA/AA/mb	5894
20988	7590	06/06/2005	EXAMINER	
OGILVY RENAULT LLP 1981 MCGILL COLLEGE AVENUE SUITE 1600 MONTREAL, QC H3A2Y3 CANADA			VENT, JAMIE J	
			ART UNIT	PAPER NUMBER
			2616	
DATE MAILED: 06/06/2005				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 09/653,701	Applicant(s) TROTIER ET AL.	
	Examiner Jamie Vent	Art Unit 2616	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 06 November 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 and 3-10 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 3-10 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION***Response to Arguments***

Applicant's arguments filed November 11, 2004 have been fully considered but they are not persuasive.

On page 5 applicant argues that Frink et al fails to teach, suggest, or disclose the following limitation: "graphics processor having at least two video inputs and a video output" as disclosed in independent Claim 1. It is noted in Figure 1 that a graphics processor 104 is shown wherein two inputs and a video output is present as further described in Column 3 Lines 30-45. Furthermore, it is noted that the system disclosed by Frink et al is an editing system as described in Column 1 Lines 12-32 and thereby providing the graphics processor in an editing environment.

On pages 5-6 applicant argues that Frink et al fails to teach, suggest, or disclose the following limitation: "... said video decoder using said first video bus to transfer data to said video encoder in a non-editing playback mode and said video decoder using said first video bus to transfer data to said codec in a video capture mode" as disclosed in Claim 2. It is noted in Column 5 Lines 20 – 33 wherein it is clearly stated that the data is sent to coder/decoder processors (codecs) from the buffer memory to reduce overhead of storage and thereby meeting the limitation. Although, applicant's points are understood the examiner can not agree and therefore the rejection is maintained.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

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(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1, 3-10 are rejected under 35 U.S.C. 102(b) as being unpatentable by

Frink et al (US 6,678,002).

[claim 1]

In regard to Claim 1, Frink et al, discloses a video editing apparatus for performing video editing in real time comprising:

- A video data storage (Figure 1F element 102 HD storage system);
- Graphics processor having at least two video inputs and a video output (Figure 1F element 104 HD video system contains a graphics processor as well as having video inputs from the storage system 102 and the HDTV video I/O 140 which is both input and output device);
- Video decoder having a video signal input and an uncompressed digital video output (Figure 2 shows the codec 216 which acts as a video decoder having a video signal from the router 220 and thereby gives an uncompressed or compressed video output to the output device 240 and further described in Column 9 Lines 35);
- Video encoder having an uncompressed video data stream input and a display signal output (Figure 2 element 206 shoes the input of the uncompressed data and sent to the output 240);
- First video bus transferring data between said video output of said graphics processor and said video encoder when said apparatus is operating in a real time video editing mode (Figure 1F shows a video

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editing system operating in real time wherein a communication bus is seen from the codec to the video data router and to the video output, line 148, in order to transfer data from these elements);

- Second video bus transferring data between said codec and at least two video inputs of the graphics processor when the apparatus is operating in a real time video-editing mode (Figure 1f shows the video inputs to the codec via buses from elements 114 and 120);
- Video decoder using one of said first and second video buses to transfer data to the video encoder in a non-editing playback mode and video decoder using one of said first and second video buses to transfer data to said codec in a video capture mode (Figure 1f shows the various data buses as well as the buses being used to transfer data back to the codec during video capture mode).

[claim 2]

In regard to Claim 2, Frink et al, discloses an apparatus wherein the video decoder transfers data to said codec and to the video encoder using the first data bus (Figure 1f shows the data bus transferring data from codec to the video encoder as shown in the local bus lines).

[claim 3]

In regard to Claim 3, Frink et al, discloses an apparatus wherein data is transferred from said video output of the graphics processor to the codec for compression and storage in the video data storage device (Figure 2 shows the codec 216 which compresses the video

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signal from the router 220 and thereby gives an uncompressed or compressed video output to the storage device 102 as further described in Column 9 Lines 35).

[claims 4 & 6]

In regard to Claims 4 and 6, Frink et al, discloses an apparatus with a graphics processor with an input buffer for storing a sequence of fields of at least two video inputs and a video output buffer for storing a sequence of fields of the graphics processor video output (Figure 1f shows the HD frame buffer 122, HD disk buffer memory 114 acts as input buffers to the graphics processor while the SDTV frame buffer acts as an output buffer for the output of the graphics processor).

[claims 5 & 7]

In regard to Claims 5 and 7, Frink et al, discloses an apparatus wherein the input buffer also stores input graphic image fields (Column 5 Lines 20-22 describe the input buffer and the storage of the graphic image fields).

[claim 8]

In regard to Claim 8, Frink et al, discloses an apparatus wherein the second video bus is a single bus communicating data multiplexed on said bus to all of at least two video input (Figure 1f shows the input of the data that has been multiplexed inputted into the buffer memory).

[claim 9]

In regard to Claim 9, Frink et al, discloses an apparatus wherein the apparatus has an input for compressed digital video input from external device, and a decompression device, one of said at least two video inputs comprising decompressed data from said compressed digital video input (Figure 2 shows the input of the compressed digital video

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via element 240 and the decompression of the data in element 204 from the compressed signal).

[claim 10]

In regard to Claim 10, Frink et al, fails to disclose the compressed digital video input comprises one of an IEEE 1394 interface and an SDTI interface.

The examiner takes official notice that it is well known in the art that compressed digital video input can have various interfaces including IEEE 1394 and SDTI. It would have been obvious to one skilled in the art at the time of the invention to incorporate these interfaces into data communication aspect of the invention disclosed by Frink et al.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

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Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jamie Vent whose telephone number is 571-272-7384.

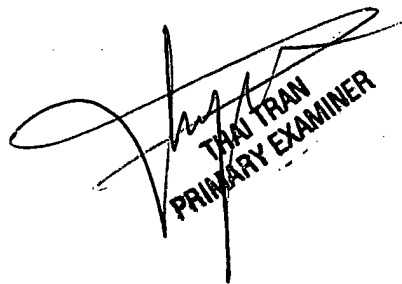
The examiner can normally be reached on 7:30am-5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, James Groody can be reached on 571-272-7950. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Jamie Vent
05/24/05



THAI TRAN
PRIMARY EXAMINER